

CLAIM AMENDMENTS

1. (currently amended) A nest for holding an integrated circuit during testing, comprising:
 - a plate having a front side and a back side;
 - an area on said front side of a cavity in the plate for receiving an integrated circuit having a plurality of pins;
 - a channel through the plate for receiving therethrough an anvil, said channel adjacent said area on said front side; and
 - an anvil detachably engaged through the channel, positioned to engage the pins of the integrated circuit and to maintain the pins in alignment.
2. (previously presented) The nest of Claim 1 wherein the plate comprises a material selected from the group consisting of aluminum, steel, or non-conductive high temperature material.
3. (original) The nest of Claim 1 wherein the nest is designed to seat a Quad Flat Pack packaged integrated circuit.
4. (original) The nest of Claim 1 wherein the anvil comprises a non-conductive, non-corrosive, frictionless material.
5. (previously presented) The nest of Claim 5 wherein the anvil comprises a non-conductive high temperature material.
6. (currently amended) A system for testing an integrated circuit comprising:
 - a test head having at least one contact point;
 - a handler for placing the integrated circuit in a position for testing; and
 - a nest connected to the handler for holding an integrated circuit during testing, comprising:
 - a plate having a front side and a back side,

an area on said front side of a cavity in the plate for receiving an integrated circuit having a plurality of pins;

a channel through the plate for receiving therethrough an anvil, said channel adjacent said area on said front side; and

an anvil detachably engaged through the channel, positioned to engage the pins of the integrated circuit and to maintain the pins in alignment.

7. (original) The system of Claim 6 wherein the anvil is slidably attached within the channel.

8. (original) The system of Claim 6 wherein the anvil is comprised of a non-conductive, non-corrosive, frictionless material.

9. (previously presented) The system of Claim 8 wherein the anvil comprises a non-conductive high temperature material.

10. (cancelled)

11. (previously presented) The system of Claim 6 wherein the plate comprises a material selected from the group consisting of aluminum, steel, or non-conductive high temperature material.

12. (cancelled)

13. (cancelled)

14. (original) The system of Claim 6 wherein the nest further comprises a means for connecting the nest to the handler.

15. (original) The system of claim 14 wherein the means for connecting are screws.

16. (original) The nest of Claim 6 wherein the nest is designed to seat a Quad Flat Pack (QFP) packaged integrated circuit.

17. (currently amended) A test apparatus comprising:

a test head having at least one contact point;

a nest adapted to hold an integrated circuit such that at least one lead on said integrated circuit can be touched by said test head contact point when said test head and said nest are juxtaposed during operation of said test apparatus, said nest comprising:

a plate;

an area on a cavity in said plate adapted to receive said integrated circuit;

a channel through said plate adjacent said area on said plate cavity in a location adjacent said test head contact point when said test head and said nest are juxtaposed; and

an anvil releasably disposed in said channel to provide force sufficient to press said lead on said integrated circuit against said test head contact point during juxtaposition of said test head and said nest.

18. (currently amended) The test apparatus of Claim 17 wherein said plate has front and back surfaces, said area on said plate being on said front surface, wherein said front surface faces facing said test head during juxtaposition of said test head and said nest, and further wherein said anvil is inserted into said channel from said back surface of said plate.

19. The test apparatus of Claim 17 further comprising a handler onto which said nest is mounted, said anvil held in place in said nest by the juxtaposition of said nest on said handler.